Open Discussion on Datacenter Efficiency: The Intel View

Community hpc-ch
Friday 9 April 2010, 12:00 - 14:30, hosted by ETH Zurich

Setting the Scope

hpc-ch is glad to announce a *talk and round table discussion* by **Mike Patterson**, Senior Power and Thermal Architect at Intel Corporation.

Michael K Patterson is a Senior Power and Thermal Architect working in the Eco-Technology Program Office in the Intel Architecture Group at Intel Corporation, Hillsboro, OR, where he works on power and thermal solutions for Intel's next-generation server, client, storage, and communications products. The work covers silicon level activity, through platform and rack level solutions, and on up to interface with Data Center power and cooling technologies. He did his undergraduate work at Purdue University, received his MS degree in Management from Rensselaer Polytechnic Institute, and was awarded his MS and PhD in Mechanical Engineering from the University of Vermont. His current technical interests include advanced closed-loop cooling systems, server power and thermal management technologies, server/datacenter interaction, and high density data center concepts. He has been with Intel for 17 years. He is a registered Professional Engineer. He is the Chairman of the Data Center Technology and Strategy Committee for the Green Grid. He is also a member of ASHRAE TC 9.9 and ASME.

Registration

The talk is open to all members and guest of hpc-ch. To attend you have to register by sending an e-mail to delorenzi(at)cscs.ch.

Location / Hosting

The Topic Forum will take place at ETH Zurich, Rämistrasse 101, 8006 Zurich in the **Building/Room HG F 33.1.**

Finding the building HG at ETH Zurich »

Floor plan »

Agenda

12:00-12:40 Registration and small buffet
12:40-12:45 Greeting and Introduction

- Vittoria Rezzonico (hpc-ch / EPF Lausanne)
- Anne Koessler (Intel Switzerland)

12:45-13:30 Keynote Presentation

- **Intel's view on Data Center design and efficiency, challenges today and in the future** - The focus lays on how to measure the efficiency and how the Datacenter owner can reduce their energy use, or apply more of it to the computational workload; Mike Patterson (Intel)

13.30-14:30 Round table discussion.
14.30 Farewell